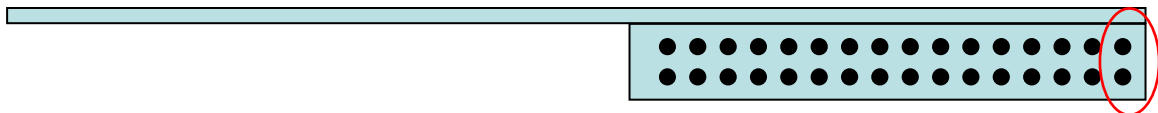


Karel Svoboda, 10/7/2007

**External cabling; corresponds to startEphys200B.m**

<b>Board, ID</b>	<b>PCI-MIO-16-4, 1</b>	<b>PCI-MIO-16-4,2</b>	<b>PCI-6713,3</b>
<i>Channel</i>	<i>Signal</i>	<i>Signal</i>	<i>Signal</i>
AD 0	scaledOutput	xMirror	
AD 1	gain telegraph	yMirror	
AD 2	Mode telegraph		
AD 3	V_hold telegraph		
AD 4			
AD 5			
AD 6			
AD 7			
DA 0	Command signal	Photodiode	shutter0
DA 1			Shutter1
DA 2	NA	NA	PockelsCell
DA 3	NA	NA	Stimulator1
DA 4	NA	NA	
DA 5	NA	NA	
DA 6	NA	NA	
DA 7	NA	NA	
DIO 0	Trigger out		
PFI 6	Trigger in from Board 1, DIO0	Trigger in from Board 1, DIO0	Trigger in from Board 1, DIO0
PFI0/Trig1	Trigger in from Board 1, DIO0	Trigger in from Board 1, DIO0	

**Internal cabling to synchronize board clocks**



This is a schematic top-view of the boards. Tie the last two pins together (frequency and Gnd)